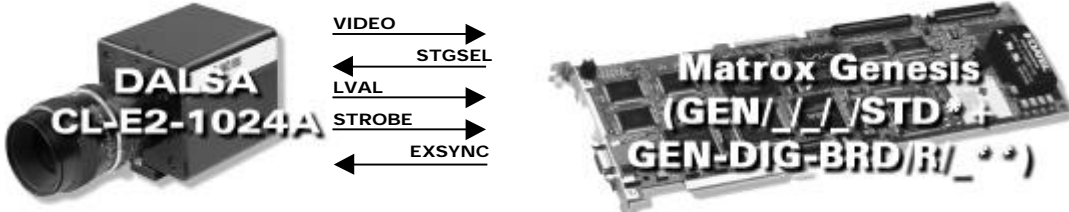
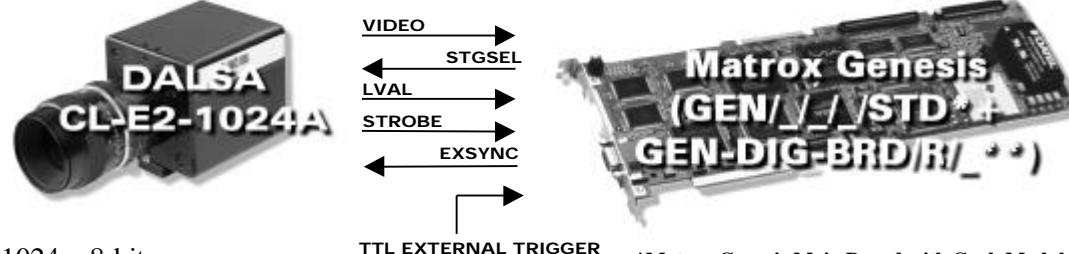


Application Note:

Interfacing non-standard cameras to Matrox Genesis

DALSA CL-E2-1024A

July 23, 1999

Camera Descriptions	<ul style="list-style-type: none"> • 1024 x 8-bit. • Single channel RS-422 digital video output. • Selectable number of TDI stages. • External synchronization required. • Maximum data rate: 20 MHz.
Interface modes	<ul style="list-style-type: none"> • Fixed line scan rate, variable line scan rate
Camera Interface Briefs	<p>Mode 1: Fixed line scan rate</p>  <p>*Matrox Genesis Main Board with Grab Module **Matrox Digital Data Input Board</p> <ul style="list-style-type: none"> • 1024 x 8-bit. • Single channel RS-422 digital video. • DCF configured for 512 lines per virtual frame. • Line scan rate is fixed and determined by EXPOSURE1 (EXSYNC) signal frequency. • Matrox Genesis sending USER0 (STGSEL) and EXPOSURE1 (EXSYNC) signals to camera; the EXPOSURE1 (EXSYNC) signal initiates line readout. • Matrox Genesis receiving PIXEL CLOCK (STROBE @ 20 MHz), HSYNC (LVAL), and video signals from camera; a high LVAL signal indicates valid pixels. • DCF used: CLE2DL.DCF <p>Mode 2: Variable line scan rate</p>  <p>*Matrox Genesis Main Board with Grab Module **Matrox Digital Data Input Board</p> <ul style="list-style-type: none"> • 1024 x 8-bit. • Single channel RS-422 digital video. • DCF configured for 512 lines per virtual frame. • Line scan rate is variable and is controlled by external trigger signal. • Matrox Genesis receiving TTL external trigger. • Matrox Genesis sending USER0 (STGSEL) and EXPOSURE1 (EXSYNC) signals to camera; the EXPOSURE1 (EXSYNC) initiates line readout. • Matrox Genesis receiving PIXEL CLOCK (STROBE @ 20 MHz), HSYNC (LVAL), and video signals from camera; a high LVAL signal indicates valid pixels. • DCF used: CLE2DAL.DCF

Application Note:

Interfacing non-standard cameras to Matrox Genesis

DALSA CL-E2-1024A

July 23, 1999

Camera Interface Details	<p>Mode 1: Fixed line scan rate</p> <ul style="list-style-type: none"> Matrox Genesis sends the EXPOSURE1 (EXSYNC) signal to the camera; the camera awaits the rising edge of the EXPOSURE1 (EXSYNC) signal and after a short (constant) delay initiates line readout. Line rate: The EXPOSURE1 (EXSYNC) period in the DCF specifies the line rate of the camera. The EXPOSURE1 (EXSYNC) period is currently set to 2010 pixels. With a 20 MHz pixel clock this translates to a 9.95 kHz line rate. Virtual frame rate: The virtual frame rate for the current settings of the DCF is 19.4 Hz. Exposure time: The time between the rising edges of the EXPOSURE1 (EXSYNC) signal is the exposure time. The default exposure time for this DCF is 100.5 μs. In order to select the exposure time, the width and deployment time of EXPOSURE1 (EXSYNC) must be set in Matrox Intellicam. The exposure time of the camera can be modified in the DCF using Matrox Intellicam, Genesis Native Library function imCamControl() or with the MIL digitizer control function MdigControl(). Refer to the appropriate manual or user guide for additional information. Maximum / minimum exposure time: Since the Matrox Genesis timer is 16-bit wide, the maximum exposure time is calculated to be $65536/20 \text{ MHz} = 3.27 \text{ ms}$. The maximum line rate of the camera is 17.0 kHz, therefore the minimum exposure time is 58.8 ms. Smallest exposure time increment: The pixel clock is the reference clock that the exposure time is being set by. The smallest increment of the exposure time is 50 ns. Stage selection: The number of TDI stages used by the camera is controlled by the USER0 (STGSEL) signal. The USER0 (STGSEL) signal value can be modified in the DCF using Matrox Intellicam, Genesis Native Library function imCamControl() or with the MIL digitizer control function MdigControl(). Refer to the appropriate manual or user guide for additional information. <div data-bbox="479 1270 1331 1522"> <p>Timing diagram for Mode 1: Fixed line scan rate. The diagram shows two signals: Exposure1 (EXSYNC) and HSYNC (LVAL). Exposure1 (EXSYNC) is a square wave with a period T1 = 100.5 ns. HSYNC (LVAL) is a square wave that starts after a delay of 56.4 ms from the rising edge of Exposure1 (EXSYNC). The duration of HSYNC (LVAL) is Video Valid = 51.2 ns. The delay between the rising edge of Exposure1 (EXSYNC) and the start of HSYNC (LVAL) is 500 ns.</p> </div> <p>Mode 2 : Variable line scan rate</p> <ul style="list-style-type: none"> Once it has received the external trigger signal, Matrox Genesis sends the EXPOSURE1 (EXSYNC) signal to the camera. A short (variable) delay will follow after receiving the EXPOSURE1 (EXSYNC), followed by the camera sending the HSYNC (LVAL) signal to the Matrox Genesis to initiate line readout. Line and virtual frame rate: Line and virtual frame are variable and controlled by the frequency of the external trigger signal.
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Application Note:

Interfacing non-standard cameras to Matrox Genesis

DALSA CL-E2-1024A

July 23, 1999

Camera Interface Details (continued)	<ul style="list-style-type: none">• Exposure time: Since the EXPOSURE1 (EXSYNC) signal is controlled by the external trigger signal, the time between the rising edges of the external trigger signal is the exposure time.• Maximum / minimum exposure time: The maximum exposure time is equal to the maximum delay between rising edges of the external trigger signal. The minimum exposure time is 58.8 ms.• Smallest exposure time: Smallest exposure time is controlled by the external trigger signal.																																																																																																																			
Cabling Requirements	<p>Mode 1: Fixed line scan rate</p> <ul style="list-style-type: none">• Matrox cable kit (GEN-TO-DALSA/16) is available for this mode as an alternative to custom cable development based on the pin-outs listing below. Note, EXPOSURE2 is connected to stage select (STGSEL), and using GEN-TO-DALSA/16 cable will require changing the polarity of EXPOSURE2 to control stage select (STGSEL).• DBHD100-TO-OPEN cable and GEN/DIG/BRD/R/_ board required for digital data, synchronization and control signals.• Connections between the 20-pin dual row connector (labeled OS1) of the camera and the 100-pin connector of the GEN-DIG-BRD/R/_ are as follows: <table><tr><th colspan="2">DALSA CL-E2-1024A</th><th></th><th colspan="2">GEN-DIG-BRD/R/_</th></tr><tr><th colspan="2">(20-pin dual row connector - OS1)</th><th></th><th colspan="2">(100-pin connector)</th></tr><tr><th><i>Pin name</i></th><th><i>Pin no.</i></th><th></th><th><i>Pin name</i></th><th><i>Pin no.</i></th></tr><tr><td>D7</td><td>01</td><td>→</td><td>DATA, INPUT, 7+</td><td>15</td></tr><tr><td>D7B</td><td>02</td><td>→</td><td>DATA, INPUT, 7-</td><td>16</td></tr><tr><td>D6</td><td>03</td><td>→</td><td>DATA, INPUT, 6+</td><td>13</td></tr><tr><td>D6B</td><td>04</td><td>→</td><td>DATA, INPUT, 6-</td><td>14</td></tr><tr><td>D5</td><td>05</td><td>→</td><td>DATA, INPUT, 5+</td><td>11</td></tr><tr><td>D5B</td><td>06</td><td>→</td><td>DATA, INPUT, 5-</td><td>12</td></tr><tr><td>D4</td><td>07</td><td>→</td><td>DATA, INPUT, 4+</td><td>09</td></tr><tr><td>D4B</td><td>08</td><td>→</td><td>DATA, INPUT, 4-</td><td>10</td></tr><tr><td>D3</td><td>09</td><td>→</td><td>DATA, INPUT, 3+</td><td>07</td></tr><tr><td>D3B</td><td>10</td><td>→</td><td>DATA, INPUT, 3-</td><td>08</td></tr><tr><td>D2</td><td>11</td><td>→</td><td>DATA, INPUT, 2+</td><td>05</td></tr><tr><td>D2B</td><td>12</td><td>→</td><td>DATA, INPUT, 2-</td><td>06</td></tr><tr><td>D1</td><td>13</td><td>→</td><td>DATA, INPUT, 1+</td><td>03</td></tr><tr><td>D1B</td><td>14</td><td>→</td><td>DATA, INPUT, 1-</td><td>04</td></tr><tr><td>D0</td><td>15</td><td>→</td><td>DATA, INPUT, 0+</td><td>01</td></tr><tr><td>D0B</td><td>16</td><td>→</td><td>DATA, INPUT, 0-</td><td>02</td></tr><tr><td>STROBE</td><td>17</td><td>→</td><td>CLOCK, INPUT, -</td><td>40</td></tr><tr><td>STROBEB</td><td>18</td><td>→</td><td>CLOCK, INPUT, +</td><td>39</td></tr><tr><td>LVAL</td><td>19</td><td>→</td><td>HSYNC, INPUT, +</td><td>33</td></tr><tr><td>LVALB</td><td>20</td><td>→</td><td>HSYNC, INPUT, -</td><td>34</td></tr></table>	DALSA CL-E2-1024A			GEN-DIG-BRD/R/_		(20-pin dual row connector - OS1)			(100-pin connector)		<i>Pin name</i>	<i>Pin no.</i>		<i>Pin name</i>	<i>Pin no.</i>	D7	01	→	DATA, INPUT, 7+	15	D7B	02	→	DATA, INPUT, 7-	16	D6	03	→	DATA, INPUT, 6+	13	D6B	04	→	DATA, INPUT, 6-	14	D5	05	→	DATA, INPUT, 5+	11	D5B	06	→	DATA, INPUT, 5-	12	D4	07	→	DATA, INPUT, 4+	09	D4B	08	→	DATA, INPUT, 4-	10	D3	09	→	DATA, INPUT, 3+	07	D3B	10	→	DATA, INPUT, 3-	08	D2	11	→	DATA, INPUT, 2+	05	D2B	12	→	DATA, INPUT, 2-	06	D1	13	→	DATA, INPUT, 1+	03	D1B	14	→	DATA, INPUT, 1-	04	D0	15	→	DATA, INPUT, 0+	01	D0B	16	→	DATA, INPUT, 0-	02	STROBE	17	→	CLOCK, INPUT, -	40	STROBEB	18	→	CLOCK, INPUT, +	39	LVAL	19	→	HSYNC, INPUT, +	33	LVALB	20	→	HSYNC, INPUT, -	34
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Application Note:

Interfacing non-standard cameras to Matrox Genesis

DALSA CL-E2-1024A

July 23, 1999

Cabling Requirements (continued)

- Connections between the DB-25 connector on the rear panel of the camera and the 100-pin connector of the GEN-DIG-BRD/R are as follows:

DALSA CL-E2-1024A (DB-25 male connector)

Pin name	Pin no.
EXSYNC	17
EXSYNCB	04
STGSEL	05
STGSELB	18
GROUND	07
GROUND	11
GROUND	20
GROUND	24

GEN-DIG-BRD/R/_ (100-pin connector)

Pin name	Pin no.
EXPOSURE, OUTPUT, 1+	95
EXPOSURE, OUTPUT, 1-	96
USER, OUTPUT, 0+	91
USER, OUTPUT, 0-	92
GROUND	50*
GROUND	37*
GROUND	38*
GROUND	50*

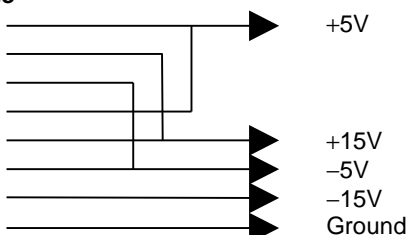
* Any pin can be used at any position: all grounds are connected on the GEN-DIG-BRD/R/_.

- Connections between the DB-25 connector on the rear panel of the camera and the power supply are as follows:

DALSA CL-E2-1024A (DB-25 male connector)

Pin no.	Pin name
8	+5V
9	+15V
12	-5V
13	+5V
21	+15V
22	-5V
25	-15V
11, 7, 20, 24	Ground

POWER SUPPLY



NOTE: it is very important that all the GROUNDS of the camera be connected together to the POWER SUPPLY GROUND, and to the GROUND of the Matrox Genesis. Do not use the cable shield as a ground, instead always use the ground pin of the power supply.

Mode 2: Variable line scan rate

- IMG-7W2-TO-5BNC and DBHD100-TO-OPEN cables, and GEN/DIG/BRD/R/_ board required for digital data, synchronization and control signals.
- TTL external trigger source should be connected to the TTL trigger input of the IMG-7W2-TO-5BNC cable
- All other connections are as in Mode 1: *Fixed line scan rate*

The DCF(s) mentioned in this application note can be found on the MIL and Native Library CD, or our FTP site ([ftp.matrox.com](ftp:ftp.matrox.com)). The information furnished by Matrox Electronics System, Ltd. is believed to be accurate and reliable. Please verify all interface connections with camera documentation or manual. Contact your local sales representative or Matrox Sales office or Matrox Imaging Applications at 514-822-6061 for assistance.

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