

JUMPER (pin to 8P4R)	P23S/P24S/ P24S/P24T	486SX/DX/ IntelDX2	Cx486SX/ (M6, M7)	IntelDX/ P240/P24CT
JC5	short	open	open	short
RN18	empty	empty	inserted	empty
RN19	empty	empty	empty	inserted
RN20	empty	empty	empty	empty

JUMPER	486SX/ P23S	P24S/P24S+/ 486DX2/ IntelDX4	P24CT/ P24T*	Cx486S (M6)	Cx486SX (M7) Cx486S+ (M6 + C6)	P24D
JC1, JC2	2-3	1-2	1-2	2-3	1-2	1-2
JC3	open	open	short	open	open	open
JC4	1-2	1-2	2-3	1-2	1-2	2-3
JC6	short	short	short	open	open	short

* P23S, P24S, P24D and P4S are the SL-enhanced CPUs while P24T is the Pentium OverDrive Processor.

→ **NOTE : When the onboard 3.3-volt regulator is not present, the 3.3-volt daughter board should be inserted. If not, please read page 2-14.**

JUMPER	PIN DEFINITION
JC7	Write-back/write-through select for P24D/P24T 1-2 write-back 2-3 write-through
JC8	For Intel's 3.3V CPU or Cynix's 3.6V CPU 1-2 IntelDX4 2-3 Cynix 486DXDX2-V
JC9	1-2 Intel-S CPU 2-3 P24D/P24T/Cynix 486DX
JC10	If onboard regulator is present, selector for Intel's 3.3V CPU or Cynix's 3.6V CPU open IntelDX4 short Cynix 486DXDX2-V

Table 2-1. Jumper Setting for CPU Selector (Continued)

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JUMPER	PIN DEFINITION
J27	DX4 clock mode select 1-2 2.5X mode 2-3 2X mode open 3X mode
JX1, J20, J23	1-2 (default)
JX2, JX5	2-3 (default)
JT3	1-2 IRQ14 (default) 2-3 IRQ10 (default)
JT4, J19	1-2 PCCLK-CPUCLK (default) 2-3 PCCLK-CPUCLK2 (40MHz is recommended for better performance)

Table 2-1. Jumper Setting for CPU Selector

→ **NOTE : Users are not encouraged to change the jumper settings not listed in this manual. Changing the jumper settings improperly may adversely affect system performance.**

NS87311/312 Jumper Setting

JF	BASE I/O ADDRESS		
	INDEX ADDRESS	DATA ADDRESS	
	1-2 26EH*	26FH*	
	2-3 398H	399H	

PRINTER PORT DIRECTION SELECT			
JUMPER	OUTPUT	INPUT	BIDIRECTION
JG	1-2*	1-2	2-3
JH	2-3*	1-2	N/A

* factory default

Table 2-2. NS87311/312 Jumper Setting

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